



**ISCAS  
2024**

# Program at a Glance

Time: UTC+8		Sunday, May 19	
07:45-08:30		<b>Registration</b> [Foyer of Taurus Room (Secretariat Room)]	
08:30-10:00	<p><b>Full-Day Tutorial #1,# 2, #3</b></p> <p>1. Mixed-Signal RF Transmitters ( <b>Instructor(s): David J. Allstot, Vanessa Chen, and Jeffrey S. Walling</b>) [Room: Pisces 2]</p> <p>2. Advanced Biomedical Imaging Technologies: Circuit Design and Techniques (<b>Instructor(s): Yuanjin Zheng, Yongfu Li, Jian Zhao, Ka-Meng Lei</b>) [Room: Pisces 1]</p> <p>3. Integrated Devices, Circuits and Systems for Quantum Computing (<b>Instructor(s): Andreas Fuhrer Janett, ChristianENZ, Andrei Vladimirescu, Fabio Sebastiano, Edoardo Charbon, Joseph Bardin, Sorin Voinigescu, Domenico Zito</b>) [Room: Leo 1]</p>	<p><b>Half-Day Tutorial (Morning) #1</b></p> <p>1. More Efforts to Developing High-Performance PLLs with Jitter Reduction Approaching Sub-10fs (<b>Instructor(s): Yong Chen (Nick)</b>) [Room: Aquarius 1]</p> <p>2. Tensor Regression: Methods and Applications (<b>Instructor(s): Yipeng Liu, Jiani Liu</b>) [Room: Aquarius 2]</p> <p>3. Energy-Efficient AI-Native Wireless Communication Systems (<b>Instructor(s): Martinez Alonso, Rodney, Martinez Alonso, Abdel</b>) [Room: Aquarius 3]</p> <p>4. Advanced Mixed Signal Concepts and Circuit Innovations Exploiting Active Bulk-Driven Techniques using 22nm FD-SOI CMOS Technology (22FDX) (<b>Instructor(s): Marcel Runge, Enne Wittenhagen, Friedel Gerfers</b>) [Room: Aquarius 4]</p> <p>5. Design of Integrated CMOS-MEMS Wireless Sensors in the Age of Intelligent Systems (<b>Instructor(s): Virgilio Valente, Nooshin Saeidi</b>) [Room: Gemini 1]</p> <p>6. Noise in Memristive Nanodevices and Circuits: A Journey from Understanding to Exploiting it (<b>Instructor(s): Vasileios Ntinias, Georgios Ch. Sirakoulis</b>) [Room: Gemini 2]</p>	
10:00-10:30		<p><b>Lunch</b> [Venue: West Lobby, Foyer beside Aquarius 1]</p> <p><b>Half-Day Tutorial (Afternoon) #3</b></p> <p>1. Using Neural Networks to Optimize the Design of Analog and Mixed-Signal Circuits and Systems (<b>Instructor(s): José M. de la Rosa</b>) [Room: Aquarius 1]</p> <p>2. How to Model the Training and Inference of Analog-Based In-Memory Computing (AIMC) Systems (<b>Instructor(s): Corey Lammie, Manuel Le Gallo, Malte Rasch</b>) [Room: Aquarius 2]</p> <p>3. Machine Learning for Automated Physical Design (<b>Instructor(s): Ioannis Savidis, Pratik Shrestha</b>) [Room: Aquarius 3]</p> <p>4. Towards Battery-free and Low-cost Distributed Sensor Node: from Novel IC Approaches to System-level Industrial Design (<b>Instructor(s): Orazio Aiello, Roberto La Rosa</b>) [Room: Aquarius 4]</p> <p>5. Hardware Security for Biomedical Circuits and Systems (<b>Instructor(s): Ibrahim (Abe) M. Elfadel</b>) [Room: Gemini 1]</p> <p>6. New Era of Artificial Intelligence: Unleashing the Power of Large Models in Visual Applications (<b>Instructor(s): Jiaying Liu, Wen-Huang Cheng, Shuai Yang</b>) [Room: Gemini 2]</p>	
10:30-12:30			
12:30-13:30			
13:30-15:00	<p><b>Welcome Reception</b> [Venue*: Gardens By the Bay (Flower Field Hall and Water View Room)]</p>		
15:00-15:30			
15:30-17:00			
18:00-21:00			

#1 – Coffee break at 10:00-10:30; #2 – Lunch at 12:30-13:30; #3 – Coffee break at 15:00-15:30

\* The venue is not located within the conference site. It takes about 20min for driving and 40min for public transportation from the conference site to there.



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# Program at a Glance

Time: UTC+8	Monday, May 20			
07:45-08:30	Registration [Foyer of Taurus Room (Secretariat Room)]			
08:30-09:00	Opening Ceremony [Venue: B2 Ballroom]			
09:00-10:00	Keynote 1: <a href="#">Aaron Thean</a> , Deputy President (Academic Affairs) and Provost, National University of Singapore, Singapore [Venue: B2 Ballroom]			
10:00-11:00	Keynote 2: <a href="#">Gert Cauwenberghs</a> , Professor, Co-Director of the Institute for Neural Computation, University of California San Diego, USA [Venue: B2 Ballroom]			
11:00-11:30	Coffee Break			
11:30-13:00	<p><b>Regular Sessions</b></p> <ol style="list-style-type: none"> <li>1. Amplifiers [Room: Aquarius 1]</li> <li>2. Models &amp; Methods for Non-Linear Circuits &amp; Systems [Room: Aquarius 2]</li> <li>3. Data Path &amp; Arithmetic Circuits and Systems [Room: Aquarius 3]</li> <li>4. Hardware Security for IoT, Circuits and Cyber-Physical Systems I [Room: Aquarius 4]</li> <li>5. Wireline Communications [Room: Gemini 1]</li> <li>6. Integrated Power Circuits &amp; Charge Pumps [Room: Gemini 2]</li> <li>7. Neural Interface Circuits &amp; Systems I [Room: Pisces 1]</li> <li>8. Neural Learning Systems: Optimizations &amp; Applications I [Room: Pisces 2]</li> <li>9. Learning-based Visual Signal Coding &amp; Processing [Room: Pisces 3]</li> </ol>	<p><b>Special Sessions</b></p> <ol style="list-style-type: none"> <li>1. Cross Society Special Session: Flexible Circuits &amp; Systems for the Era of Everything Intelligence [Room: Pisces 4]</li> <li>2. Inversion Coefficients &amp; Ratio-based (gm/ID, gm/Cg, etc.) Design Methodologies [Room: Virgo 1]</li> <li>3. Novel Hardware Implementation of Learning Algorithms in Deep &amp; Spiking Neural Networks I [Room: Virgo 2]</li> <li>4. RFIC &amp; AI: Pioneering New Wireless Communications [Room: Virgo 3]</li> </ol>	<p><b>Workshop/Other</b></p> <ol style="list-style-type: none"> <li>1. Student Design Competition [Room: Leo 1]</li> </ol>	<p><b>Poster/Demo</b></p> <ol style="list-style-type: none"> <li>1. Poster (11 Sessions)</li> <li>2. Live Demo I</li> </ol> <p>[Room: Leo 2, 3, 4]</p>
13:00-14:00	Lunch [Venue: B2 Ballroom]			



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Time: UTC+8		Monday, May 20 (Continue)		
14:00-15:00	<b>Keynote 3:</b> <a href="#">Sandro Carrara</a> , Professor, Ecole Polytechnique Fédérale de Lausanne (EPFL), Switzerland [Venue: B2 Ballroom]			
15:00-16:30	<p align="center"><b><u>Regular Sessions</u></b></p> <ol style="list-style-type: none"> <li>LDO Regulators [Room: Aquarius 1]</li> <li>AI &amp; ML Techniques for Non-Linear Circuits &amp; Systems [Room: Aquarius 2]</li> <li>Low Power Logic, Circuits &amp; Architectures I [Room: Aquarius 3]</li> <li>Digital Circuits, Systems &amp; Architecture for Machine Learning I [Room: Aquarius 4]</li> <li>Optical Communications [Room: Gemini 1]</li> <li>Circuits &amp; Systems for Energy Harvesting [Room: Gemini 2]</li> <li>Neural Interface Circuits &amp; Systems II [Room: Pisces 1]</li> <li>Neural Learning Systems: Transformers &amp; Applications I [Room: Pisces 2]</li> <li>Image/Video Coding &amp; Standardization [Room: Pisces 3]</li> </ol>	<p align="center"><b><u>Special Sessions</u></b></p> <ol style="list-style-type: none"> <li>Optical &amp; Wireless Communication &amp; Sensing Technologies in Terrestrial &amp; Non-Terrestrial Systems for 6G I [Room: Pisces 4]</li> <li>Innovations in Computational Intelligence: Studies on Structures, Detection, &amp; Optimization [Room: Virgo 1]</li> <li>Novel Hardware Implementation of Learning Algorithms in Deep &amp; Spiking Neural Networks II [Room: Virgo 2]</li> <li>Delta-Sigma ADCs &amp; its AI Application [Room: Virgo 3]</li> </ol>	<p align="center"><b><u>Workshop/Other</u></b></p> <ol style="list-style-type: none"> <li>Climate Change Workshop [Room: Leo 1]</li> </ol>	N/A
16:30-17:00	<b>Coffee Break</b>			<b><u>Poster/Demo/Competition</u></b>
17:00-18:30	<p align="center"><b><u>Regular Sessions</u></b></p> <ol style="list-style-type: none"> <li>Analog Signal Processing I [Room: Aquarius 1]</li> <li>Sigma Delta Modulator for ADC [Room: Aquarius 2]</li> <li>Low Power Logic, Circuits &amp; Architectures II [Room: Aquarius 3]</li> <li>Digital Circuits, Systems &amp; Architecture for Machine Learning II [Room: Aquarius 4]</li> <li>Cryptography &amp; Hardware Security [Room: Gemini 1]</li> <li>Circuits &amp; Systems for Wireless Power Transfer Applications [Room: Gemini 2]</li> <li>Machine Learning &amp; Signal Processing for Biomedical Systems I [Room: Pisces 1]</li> <li>Neural Learning Systems: Techniques &amp; Applications I [Room: Pisces 2]</li> <li>Deep Learning for Visual Signal Representation &amp; Processing [Room: Pisces 3]</li> </ol>	<p align="center"><b><u>Special Sessions</u></b></p> <ol style="list-style-type: none"> <li>Optical &amp; Wireless Communication &amp; Sensing Technologies in Terrestrial &amp; Non-Terrestrial Systems for 6G II [Room: Pisces 4]</li> <li>Improving Student Retention &amp; Use of AI/ChatGPT in Engineering Education [Room: Virgo 1]</li> <li>Various Synchronization in Coupled Nonlinear Circuits with Specialized Coupling &amp; Applications [Room: Virgo 2]</li> <li>AI-Based Detection &amp; Estimation for Health &amp; Security Applications [Room: Virgo 3]</li> </ol>	<p align="center"><b><u>Workshop/Other</u></b></p> <ol style="list-style-type: none"> <li>Climate Change Workshop [Room: Leo 1]</li> </ol>	<ol style="list-style-type: none"> <li>Poster (4 Sessions)</li> <li>PhD Forum</li> <li>Student Design Competition Demo</li> </ol> <p align="center">[Room: Leo 2, 3, 4]</p>
18:30-22:00	<b>WiCAS-YP Event</b> [Room: Leo 1]			



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# Program at a Glance

Time: UTC+8		Tuesday, May 21		
07:45-08:30	<b>Registration</b> [Foyer of Taurus Room (Secretariat Room)]			
08:30-09:00	<b>CASS 75<sup>th</sup> Anniversary</b> [Room: Leo 1]			
09:00-10:00	<b>Past President Sharing Panel</b> [Room: Leo 1]			
10:00-10:30	<b>Coffee Break</b>			<b>Poster/Demo</b>
10:30-12:00	<p><b>Regular Sessions</b></p> <ol style="list-style-type: none"> <li>Fractional N &amp; All Digital PLL [Room: Aquarius 1]</li> <li>ADC/DAC Circuits [Room: Aquarius 2]</li> <li>Advanced Memory &amp; Computing-in-Memory Circuits I [Room: Aquarius 3]</li> <li>Digital Circuits, Systems &amp; Architecture for Machine Learning III [Room: Aquarius 4]</li> <li>Wireless Communications I [Room: Gemini 1]</li> <li>Modelling &amp; Control of Power &amp; Energy Circuits &amp; Systems [Room: Gemini 2]</li> <li>Multimedia Systems for Coding &amp; Processing [Room: Pisces 1]</li> <li>Neuromorphic Spiking Learning Systems &amp; Applications I [Room: Pisces 2]</li> </ol>	<p><b>Special Sessions</b></p> <ol style="list-style-type: none"> <li>Compact Smart Wearable Devices &amp; Digital Health [Room: Pisces 4]</li> <li>Grand Challenge on Neural Network-based Video Coding [Room: Virgo 1]</li> <li>Trustable &amp; Sustainable Intelligent Circuits &amp; System Design [Room: Virgo 3]</li> </ol>	<p><b>Workshop/Other</b></p> <ol style="list-style-type: none"> <li>CASS Standards Association Workshop [Room: Virgo 4]</li> <li>Info Security Workshop [Room: Leo 1]</li> <li>ISCAS PhD Forum [Room: Pisces 3]</li> <li>12th International Workshop on Computational Intelligence for Multimedia Understanding [Room: Virgo 2]</li> </ol>	<ol style="list-style-type: none"> <li>Poster (12 Sessions)</li> <li>Special Live Demo</li> </ol> <p>[Room: Leo 2, 3, 4]</p>
12:00-13:00	<b>Keynote 4:</b> <a href="#">Huiming Bu</a> , Vice President, IBM Semiconductors Global R&D and Albany Operations, IBM Research, USA [Venue: B2 Ballroom]			
13:00-14:00	<b>Lunch</b> [Venue: B2 Ballroom]			



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**Time: UTC+8**

**Tuesday, May 21 (Continue)**

14:00-15:30

**Regular Sessions**

1. High Frequency PLLs & Oscillators [Room: Aquarius 1]
2. ADC Circuit Techniques [Room: Aquarius 2]
3. Advanced Memory & Computing-in-Memory Circuits II [Room: Aquarius 3]
4. Digital Circuits, Systems & Architecture for Machine Learning IV [Room: Aquarius 4]
5. 6G, IoT Systems & Sensor Networks I [Room: Gemini 1]
6. High-Efficiency Power Converters & Drive Circuits [Room: Gemini 2]
7. Deep Learning in Multimedia Applications [Room: Pisces 1]
8. Neuromorphic Spiking Learning Systems & Applications II [Room: Pisces 2]
9. Signal Processing for Sensor Arrays & Networks [Room: Pisces 3]

**Special Sessions**

1. Emerging Technologies in Neural Prosthetic & Bio-inspired Devices [Room: Pisces 4]
2. Emerging Non-Volatile Devices for Computing [Room: Virgo 1]
3. Technology & Agribusiness [Room: Virgo 2]
4. Physical Hardware Evaluation from Design Trust to System Reliability [Room: Virgo 3]

**Workshop/Other**

1. CASS Standards Association Workshop [Room: Virgo 4]
2. Info Security Workshop [Room: Leo 1]

**Poster/Demo**

1. Poster (11 Sessions)
2. Live Demo II [Room: Leo 2, 3, 4]

15:30-16:00

**Coffee Break**

16:00-17:00

**Keynote 5:** [Michael Tse](#), Chair Professor of Electrical Engineering and Associate Vice President at City University of Hong Kong, Hong Kong [Venue: B2 Ballroom]

17:00-17:45

**Award Ceremony** [Venue: B2 Ballroom]

19:00-22:00

**Gala Dinner** [Venue: B2 Ballroom]



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# Program at a Glance

Time: UTC+8	Wednesday, May 22			
07:45-09:00	<b>Registration</b> [Foyer of Taurus Room (Secretariat Room)]			
09:00-10:30	<b>Regular Sessions</b>	<b>Special Sessions</b>	<b>Workshop/Other</b>	N/A
	1. Voltage Regulators & Current Reference [Room: Aquarius 1] 2. Memory Circuits & Interconnects [Room: Aquarius 2] 3. SOC, NOC, Multi-Core, & 3D/2.5D Systems [Room: Aquarius 3] 4. Circuit Techniques for Computing-in-Memory & Machine Learning [Room: Aquarius 4] 5. Quantum Computing Circuits & Systems I [Room: Gemini 1] 6. Education in Circuits & Systems I [Room: Gemini 2] 7. Biomedical Circuits & Systems I [Room: Pisces 1] 8. Neuromorphic Systems I [Room: Pisces 2] 9. Image Processing [Room: Pisces 3]	1. Brain Computer Interface: Algorithm & Signal Processing [Room: Pisces 4] 2. Improving the Accuracy & Reliability of Analog-Based In-Memory Computing Systems I [Room: Virgo 1] 3. Smart 6G Wireless Baseband: Design & Implementations [Room: Virgo 2] 4. Efficient Processing of Large Language Models at the Edge [Room: Virgo 3]	1. AutoCAS Workshop [Room: Leo 1] 2. 3D Integration & Advanced Packaging Workshop [Room: Virgo 4]	
10:30-11:00	<b>Coffee Break</b>			<b>Poster/Demo</b>
11:00-12:30	<b>Regular Sessions</b>	<b>Special Sessions</b>	<b>Workshop/Other</b>	1. Poster (11 Sessions) 2. Live Demo III [Room: Leo 2, 3, 4]
	1. Analog Techniques I [Room: Aquarius 1] 2. Voltage Reference Circuits [Room: Aquarius 2] 3. Programmable & Reconfigurable Array Architectures [Room: Aquarius 3] 4. Ultra-low Power Circuits & Systems [Room: Aquarius 4] 5. Advanced CMOS, Cryogenics and 3D Integration [Room: Gemini 1] 6. Dynamic & Event-Driven Vision Sensors [Room: Gemini 2] 7. Biomedical Circuits & Systems II [Room: Pisces 1] 8. Neuromorphic Systems II [Room: Pisces 2] 9. Filter Design, Implementation & Application [Room: Pisces 3]	1. Brain Computer Interface: Hardware & Circuit Design [Room: Pisces 4] 2. Improving the Accuracy & Reliability of Analog-Based In-Memory Computing Systems II [Room: Virgo 1] 3. Recent Progress in Analysis & Estimation of Bifurcation Phenomena [Room: Virgo 2] 4. Ultra-Low-Power ICs Enabling Sensor Nodes Without Batteries [Room: Virgo 3]	1. AutoCAS Workshop [Room: Leo 1] 2. 3D Integration & Advanced Packaging Workshop [Room: Virgo 4]	
12:30-13:30	<b>Lunch</b> [Venue: B2 Ballroom]			



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# Program at a Glance

Time: UTC+8		Wednesday, May 22 (Continue)		
13:30-15:00	<p align="center"><b><u>Regular Sessions</u></b></p> <ol style="list-style-type: none"> <li>1. Photonics &amp; mm-Wave Circuits [Room: Aquarius 1]</li> <li>2. RF &amp; mm-Wave Circuits I [Room: Aquarius 2]</li> <li>3. Hardware Security for Logic, Circuits &amp; Architectures I [Room: Aquarius 3]</li> <li>4. Advanced Techniques for Digital Integrated Circuits &amp; Systems I [Room: Aquarius 4]</li> <li>5. Computing with Emergent Technologies II [Room: Gemini 1]</li> <li>6. Sensory Signals Processing Circuits [Room: Gemini 2]</li> <li>7. Wearable Biomedical Circuits &amp; Systems I [Room: Pisces 1]</li> <li>8. Neural Memristive In-Memory Computation Systems [Room: Pisces 2]</li> <li>9. Machine Learning for Speech &amp; Language Processing [Room: Pisces 3]</li> </ol>	<p align="center"><b><u>Special Sessions</u></b></p> <ol style="list-style-type: none"> <li>1. Intelligent &amp; Data Analytics to Real-Life Complex Networks &amp; Nonlinear Systems I [Room: Pisces 4]</li> <li>2. Artificial Intelligence in Power &amp; Energy Circuits &amp; Systems I [Room: Virgo 1]</li> <li>3. Emerging AI-driven Visual Computing &amp; Multimodal Learning for Real-world Applications [Room: Virgo 2]</li> <li>4. Theory &amp; Applications of Memristor Devices, Circuits, &amp; Systems for Bio-Inspired Electronics I [Room: Virgo 3]</li> </ol>	<p align="center"><b><u>Workshop/Other</u></b></p> <ol style="list-style-type: none"> <li>1. GeronCAS Workshop [Room: Leo 1]</li> <li>2. 3D Integration &amp; Advanced Packaging Workshop [Room: Virgo 4]</li> </ol>	N/A
15:00-15:30	<b>Coffee Break</b>			<b><u>Poster/Demo</u></b>
15:30-17:00	<p align="center"><b><u>Regular Sessions</u></b></p> <ol style="list-style-type: none"> <li>1. Analog Techniques II [Room: Aquarius 1]</li> <li>2. Time Interleaved &amp; SAR ADC [Room: Aquarius 2]</li> <li>3. Hardware Security for Logic, Circuits &amp; Architectures II [Room: Aquarius 3]</li> <li>4. Electronic Design Automation &amp; Physical Design I [Room: Aquarius 4]</li> <li>5. Computing with Emergent Technologies I [Room: Gemini 1]</li> <li>6. 2D/3D Image Sensors [Room: Gemini 2]</li> <li>7. Lab-on-Chip &amp; Point-of-Care Biomedical Diagnostics [Room: Pisces 1]</li> <li>8. Biomedical Signal &amp; Image Processing [Room: Pisces 3]</li> </ol>	<p align="center"><b><u>Special Sessions</u></b></p> <ol style="list-style-type: none"> <li>1. Intelligent &amp; Data Analytics to Real-Life Complex Networks &amp; Nonlinear Systems II [Room: Pisces 4]</li> <li>2. Millimeter-Wave &amp; Sub-THz 5G/6G/SATCOM Broadband Circuits &amp; Systems [Room: Virgo 1]</li> </ol>	<p align="center"><b><u>Workshop/Other</u></b></p> <ol style="list-style-type: none"> <li>1. GeronCAS Workshop [Room: Leo 1]</li> <li>2. 3D Integration &amp; Advanced Packaging Workshop [Room: Virgo 4]</li> </ol>	<ol style="list-style-type: none"> <li>1. Poster (10 Sessions) [Room: Leo 2, 3, 4]</li> </ol>
18:00-21:00	<b>Farewell Reception</b> [Venue: Malaysian Food Street at Resorts World Sentosa]			